



#6
M.H.
7-15-03

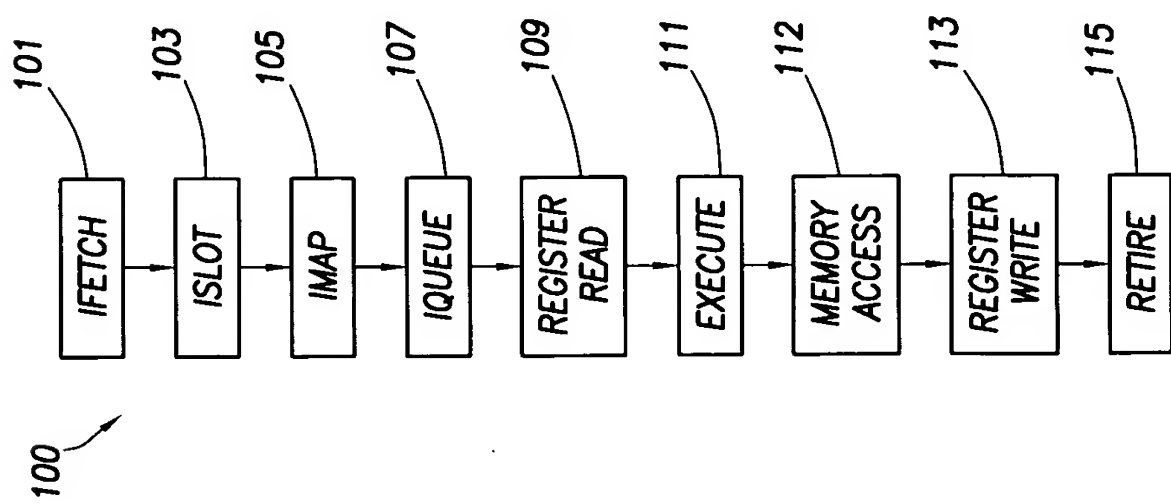


FIG. 1

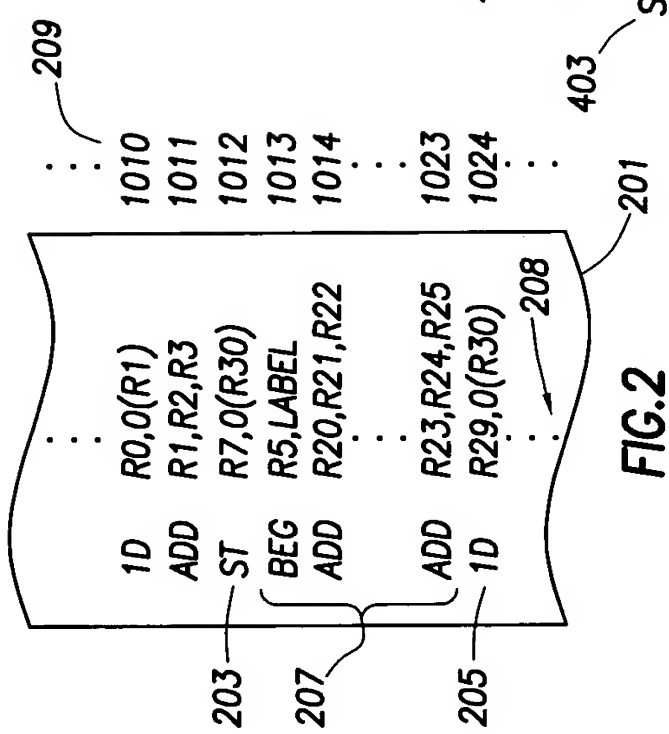


FIG. 2

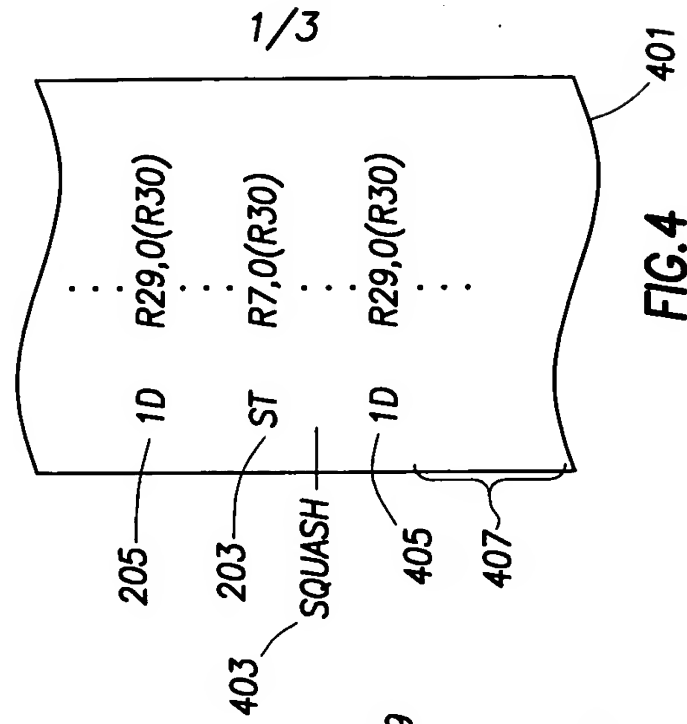


FIG. 4

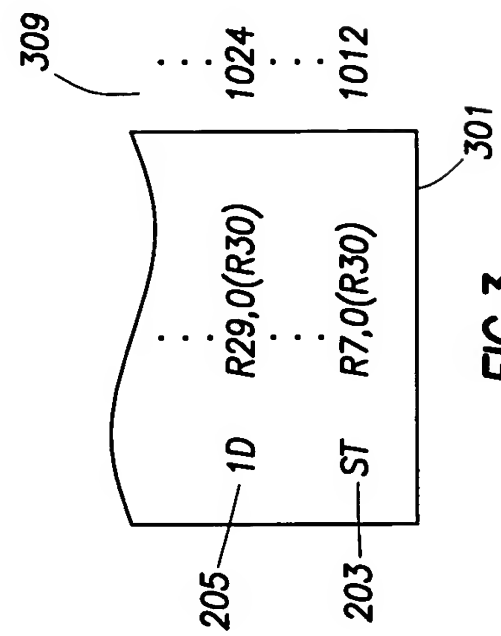
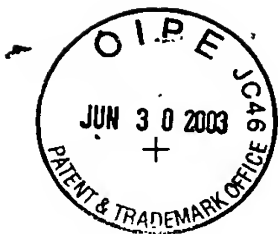
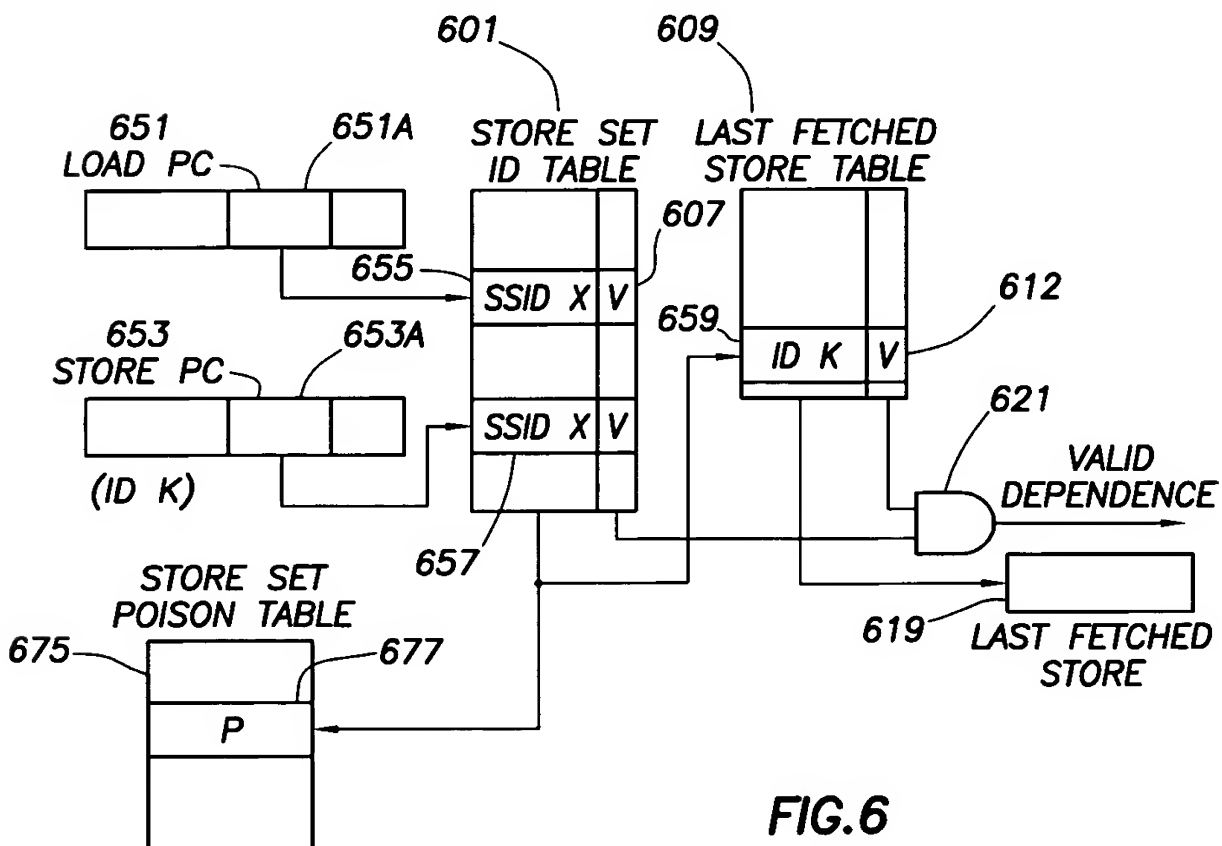
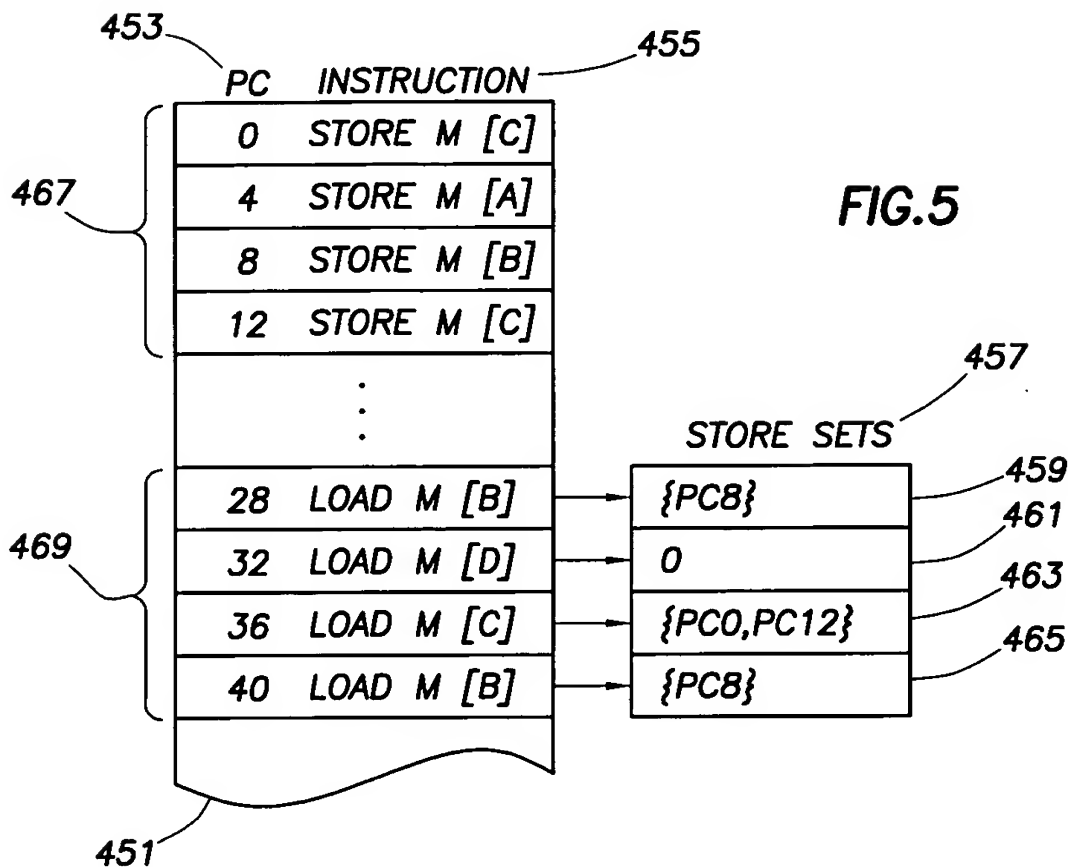


FIG. 3



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FIG. 7

LOAD1=LD MEM(y)→R1
⋮
STORE1=ST R1→MEM(x)
⋮
LOAD2=LD MEM(x)→R3

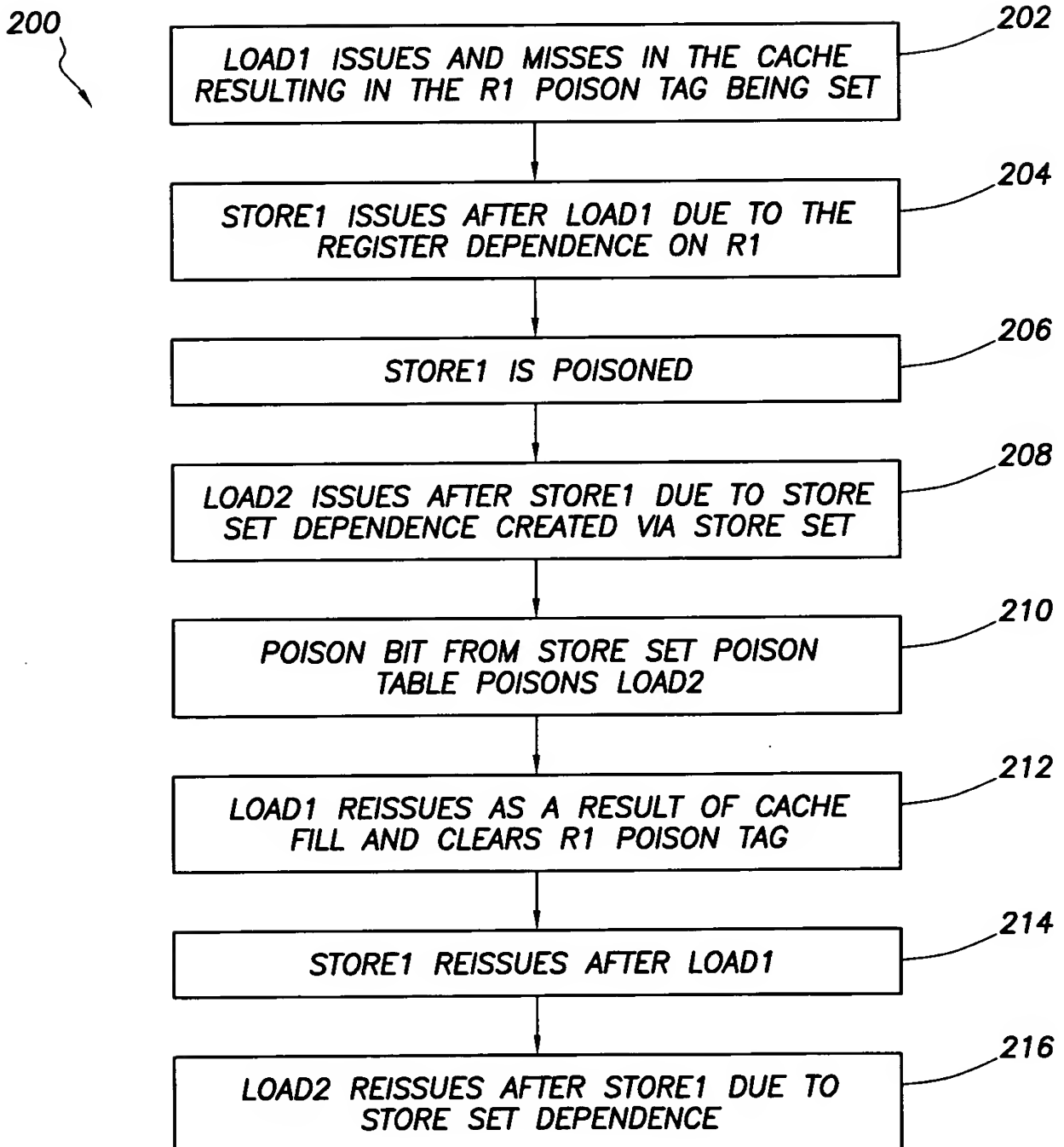


FIG. 8